



07 869851

#17/67

501.20699VC2

Am 40
6/11/92

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): R. HORI, et al.
Serial No.: to be assigned
Filed: April 16, 1992
For: SEMICONDUCTOR INTEGRATED CIRCUIT
Group: 236
Examiner: S. Baker

PRELIMINARY AMENDMENT

Honorable Commissioner of Patents
and Trademarks
Washington, DC 20231

April 16, 1992

Sir:

The following amendments and remarks are submitted
prior to examination of the Rule 62 Continuation application
filed April 16, 1992.

IN THE CLAIMS:

7.2. (amended) A semiconductor integrated circuit
comprising:
a chip;
first circuits provided on said chip;
second circuits provided on said chip; and
internal power supply means provided on said chip
for reducing an external supply voltage to an internal
supply voltage [lower] smaller than said external supply
voltage within said chip;

wherein, when the magnitude of said external
supply voltage is not larger [higher] than that of a

G